

Figure 1

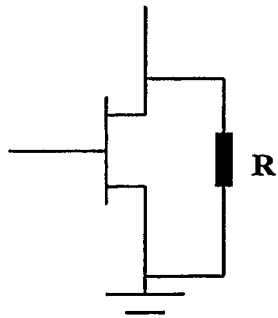


Figure 2

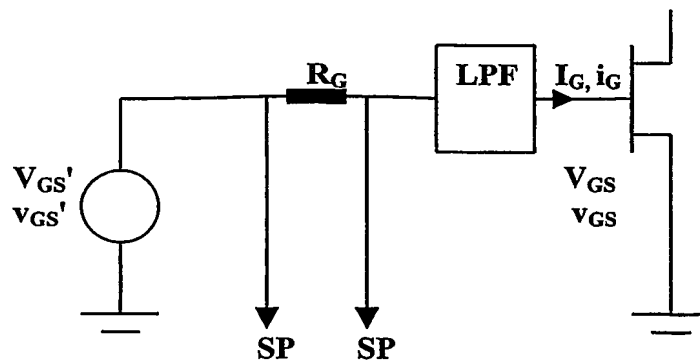


Figure 3

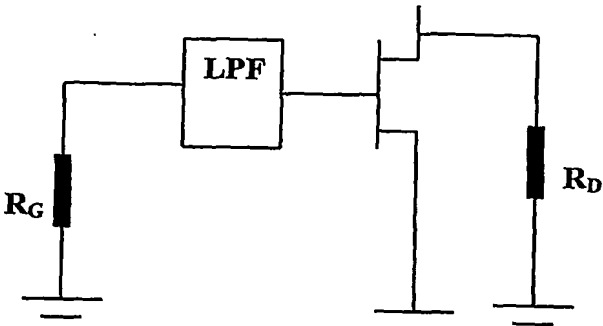


Figure 4

Input generator

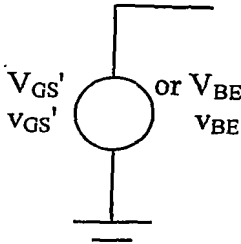


Figure 5

Input op amp

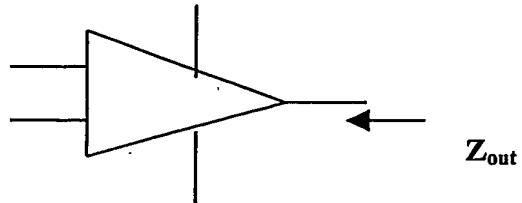


Figure 6

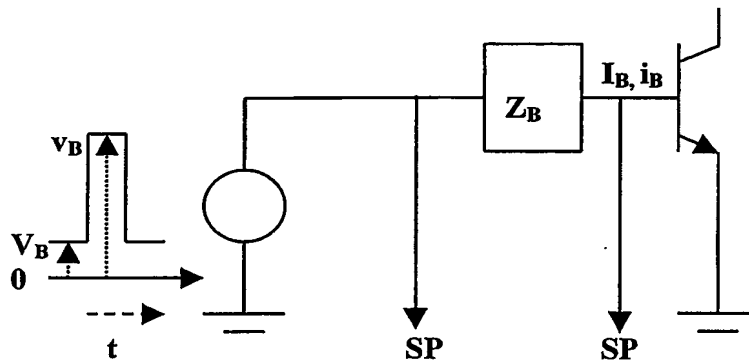


Figure 7

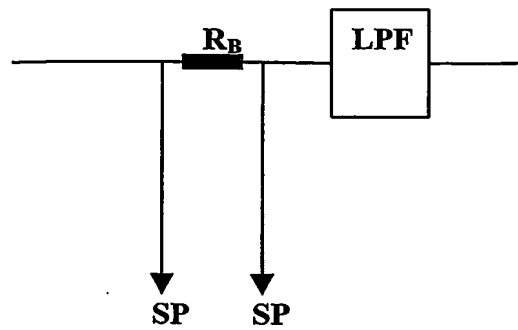


Figure 8

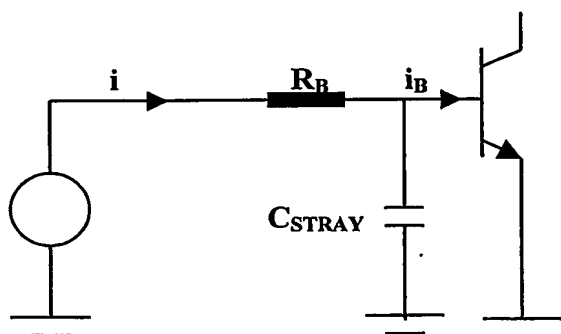


Figure 9

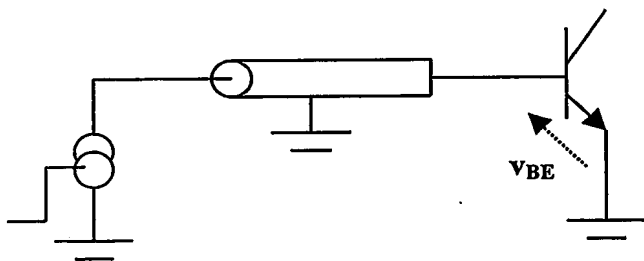


Figure 10

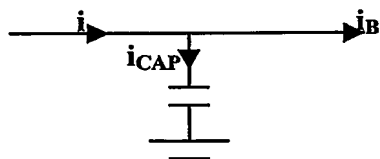


Figure 11

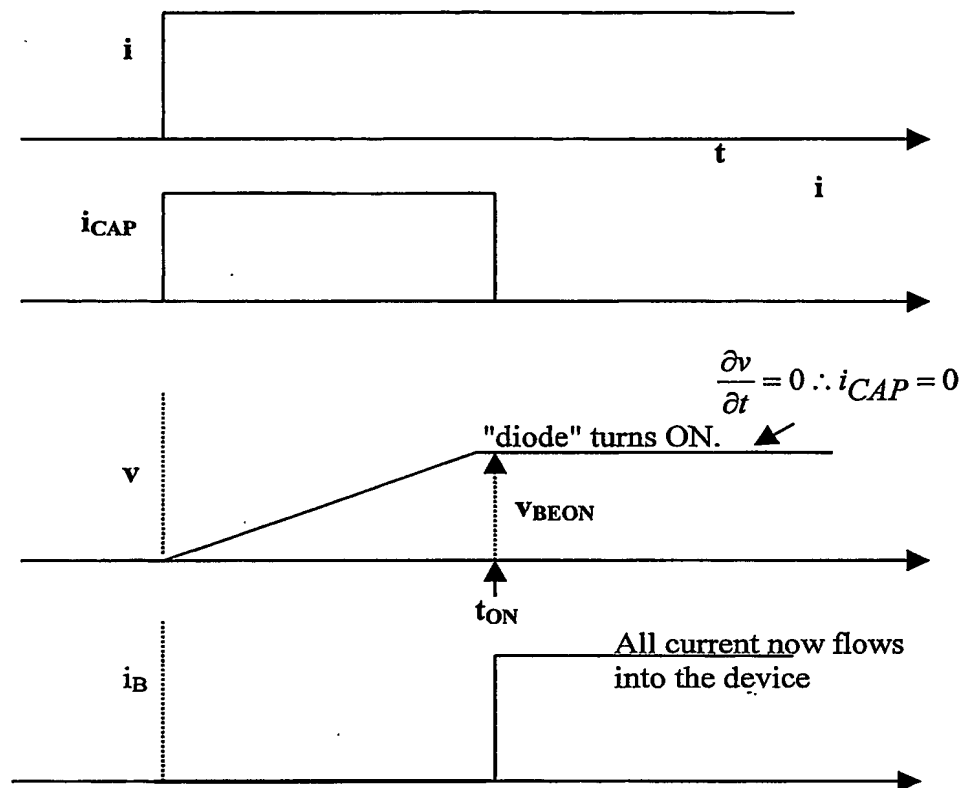


Figure 12

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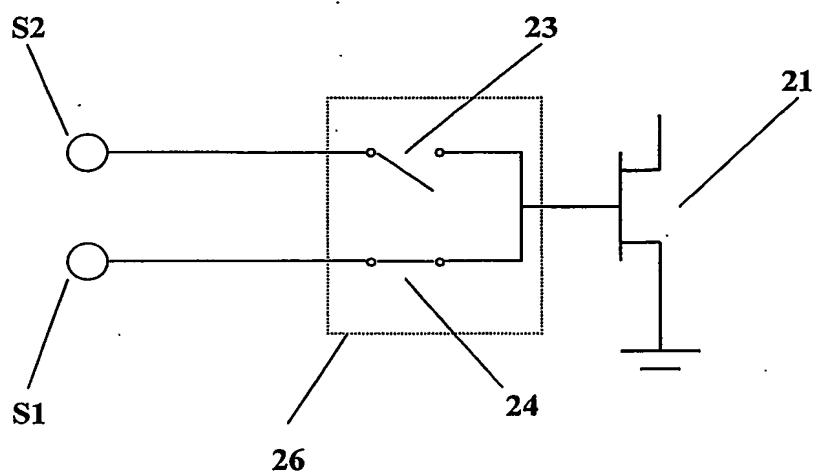


Figure 13

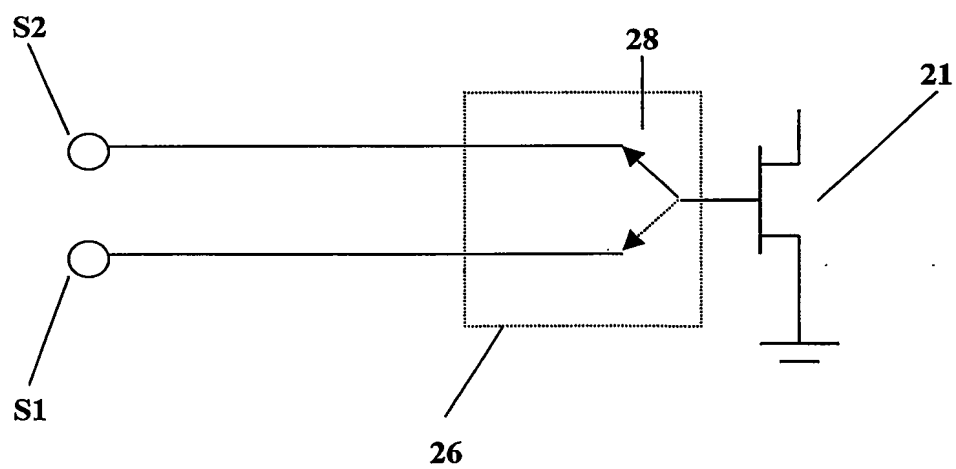


Figure 14

Figure 15a

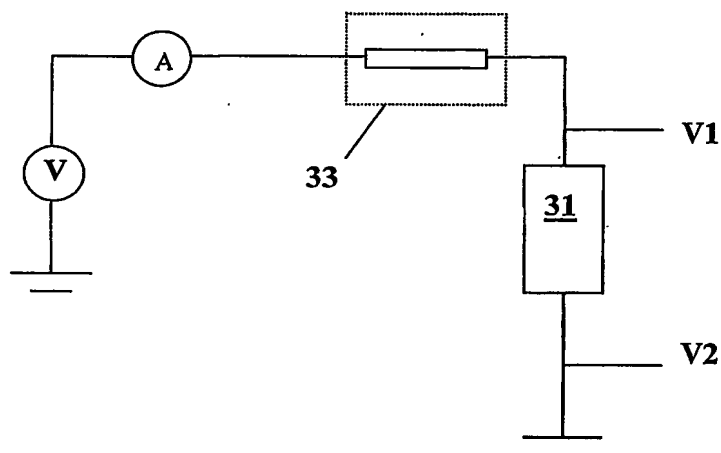


Figure 15b

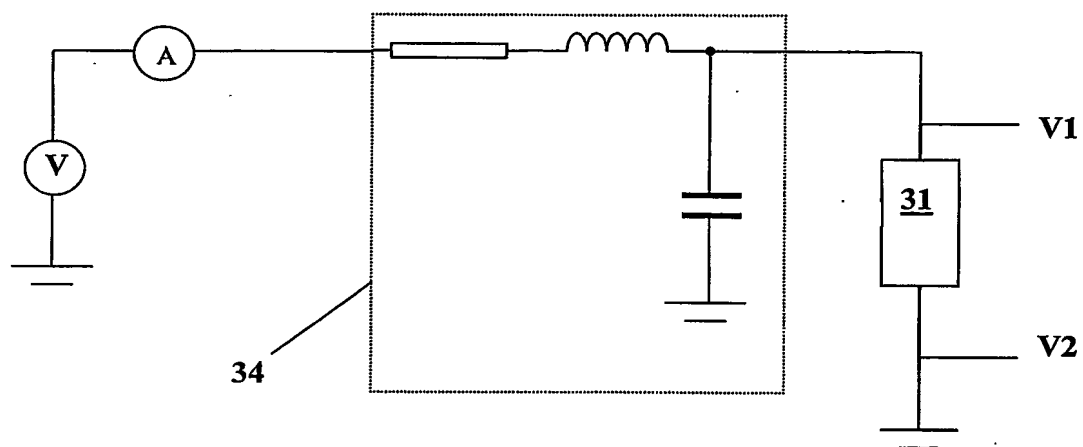


Figure 15c

